GURU NANAK INSTITUTE OF TECHNOLOGY An Autonomous Institute under MAKAUT 2022

Microprocessors & Microcontrollers E1402

TIME ALLOTTED: 3 HOURS

FULL MARKS: 70

The figures in the margin indicate full marks.

Candidates are required to give their answers in their own words as far as practicable

GROUP - A

(Multiple Choice Type Questions)

Answer any ten from the following, choosing the correct alternative of each question: 10×1=10

| 1.(i) | A 16 bit address bus can generate | address | Marks | CO No. |
|-------|--|-----------------------------------|-------|--------|
| | a) 32767 | address. | | |
| | b) 25652 | | | |
| | c) 65536 | | | |
| | d) 32 | | | |
| (ii) | System bus contain | | 1 | CO1 |
| | a) address bus | | | |
| | b) data bus | ¥ | | |
| | c) control bus | 2 | | |
| | d) all of these | | | |
| (iii) | Which one of the following is not a vect | ored interrupt | 1 | CO3 |
| | a) TRAP | | | |
| | b) INTR | | | |
| | c) RST 7.5 | | | |
| | d) all of these | | | |
| (iv) | Which one of the following registers of register | 8085 microprocessor is a 16 bit | 1 | COI |
| | a) B | | | |
| | b) H | | | |
| | c) L | | | |
| | d) Program Counter | | | |
| (v) | Which one of the following instructions complement the accumulator content | of 8085 microprocessor is used to | 1 | CO2 |
| | a) DAA | | | |
| | b) CMA | | | |
| | c) HLT | | | |
| | d) EI | | | |

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| (vi) | Which of the following instructions of 8085 is comes under immediate addressing mode | 1 | CO2 |
|--------|---|---|-----|
| | a) MOV B,C | | |
| | b) ADD D | | |
| | c) RST-1 | | |
| | d) MVIB, 02H | | |
| (vii) | The contents of the Program Counter (PC), when the microprocessor is reading from 2FFF H memory location, will be a) 2FFE H | 1 | CO1 |
| | b) 2FFF H | | |
| | c) 3000 H | | |
| | d) 3001 H | | |
| (viii) | Which one of the following instructions is a 3-byte instruction? | 1 | CO2 |
| | a) MVIA | | |
| | b) LDAX B | | |
| | c) JMP 2050 H | | |
| | d) MOV A,M | | |
| (ix) | | 1 | CO2 |
| | Intel microprocessor. | | |
| | a) Memory write cycle | | |
| | b) Operation code fetch cycle | | |
| | c) Memory Read cycle | | |
| | d) Input/output read cycle | | |
| (x) | 8086 microprocessor is | 1 | CO1 |
| | a) 16 bit processor | | |
| | b) 8 bit processor | | |
| | c) 32 bit processor | | |
| | d) 64 bit processor | | |
| (xi) | The size of internal RAM of 8051 is | 1 | COI |
| | a) 16 bytes | | |
| | b) 32 bytes | | |
| | c) 64 bytes | | |
| | d) 128 bytes | | |
| (xii) | In 8051, if RS0 = 1 and RS1 = 0, then which register bank is selected | 1 | COI |
| | a) Bank0 | | |
| | b) Bank1 | | |
| | c) Bank2 | | |
| | d) Bank3 | | |

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GROUP – B (Short Answer Type Questions) (Answer any *three* of the following) $3 \times 5 = 15$

| | | (Answer any <i>three</i> of the following) $3 \times 5 = 15$ | | |
|-----|--------------------|--|------------|-----------------|
| 2. | Dr | aw the timing diagram of-(i) MVI B,06H | Marks 5 | CO No. |
| 3. | | scribe the function of the following pins of 8085 microprocessor | 5 | CO1 |
| | ii. iii. iv. | ALE HOLD HLDA INTA RESET | | |
| 4. | Но | w does ALE signal demultiplex the AD0 – AD7 bus? Explain with gram | 5 | CO1 |
| 5. | | aw the flag register of 8086 and explain the function of each flag. | 5 | CO4 |
| 6. | | nat is Program Status Word (PSW) in 8051? Explain bit-wise. | 5 | COI |
| | | GROUP - C | | |
| | | (Long Answer Type Questions) (Answer any <i>three</i> of the following) $3 \times 15 = 45$ | | |
| | | | Marks | CO No. |
| 7. | a) | Draw the timing diagram of: 8090H STA, 8001H | 8 | CO2 |
| | b) | Define instruction cycle, machine cycle & T-state. | 3 | CO2 |
| 8. | c) a) | Write an assembly language program for 8085 microprocessor, to add two 8 bit numbers stored in the memory locations and 8000H and 8001H, and store the result in 8002H sum being 16 bit. Draw the necessary hardware to generate different RST codes in priority basis and explain. | 5 | CO2 |
| | b) | What are the functions of the following instructions-(i) EI (ii) SIM | 5 | CO1 |
| | c) | Explain the different modes of addressing in 8085 with proper | 5 | CO1 |
| | C) | examples and explanation. | 3 | COI |
| 9. | a) | Draw the control word for I/O mode in 8255 and explain bit wise. | 6 | CO3 |
| | b) | Explain the microprocessor initiated operations in 8085. | 5 | COI |
| | c) | Explain how bidirectional communication can be done between two computer using 8255 | 4 | CO3 |
| 10. | a) | Draw and explain the architecture of 8086. | 6 | COI |
| | b) | Explain the signals associated with minimum mode in 8086 | 4 | CO4 |
| | c) | What is pipelining in 8086? How many memory segments are in 8086? Explain the method of physical address generation of 8086. | 5 | CO1,CO |
| 11. | a) | Draw the structure of internal RAM of 8051. | 5 | CO1 |
| | b) | Explain the special function registers of 8051. | 5 | CO ₁ |
| | c) | Write short note on Timers in 8051. | 5 | COI |